

Table 5. SD_MODE Control

SD_MODE STATUS	SELECTED CHANNEL
High	Left
Pullup through R _{SMALL}	Right
Pullup through R _{LARGE}	(Left2 + right2)
Low	Shutdown

Table 6. Examples of SD_MODE Pullup Resistor Values

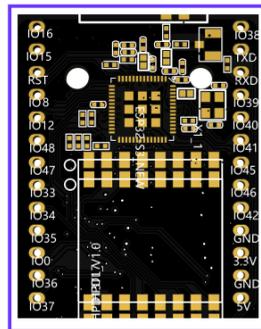
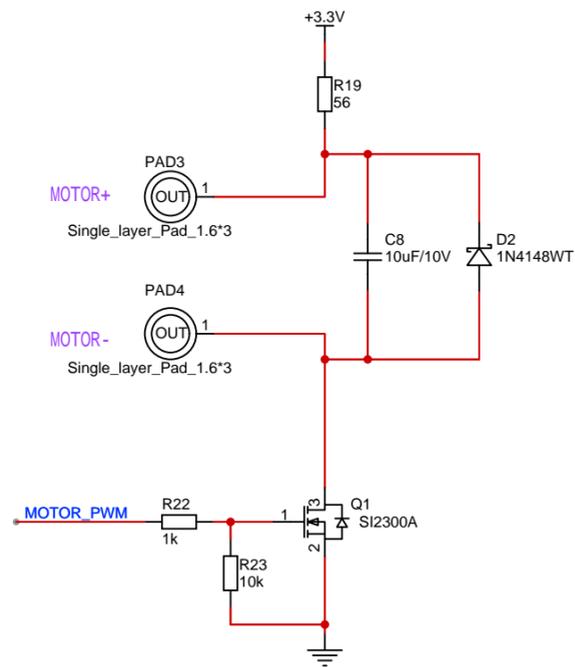
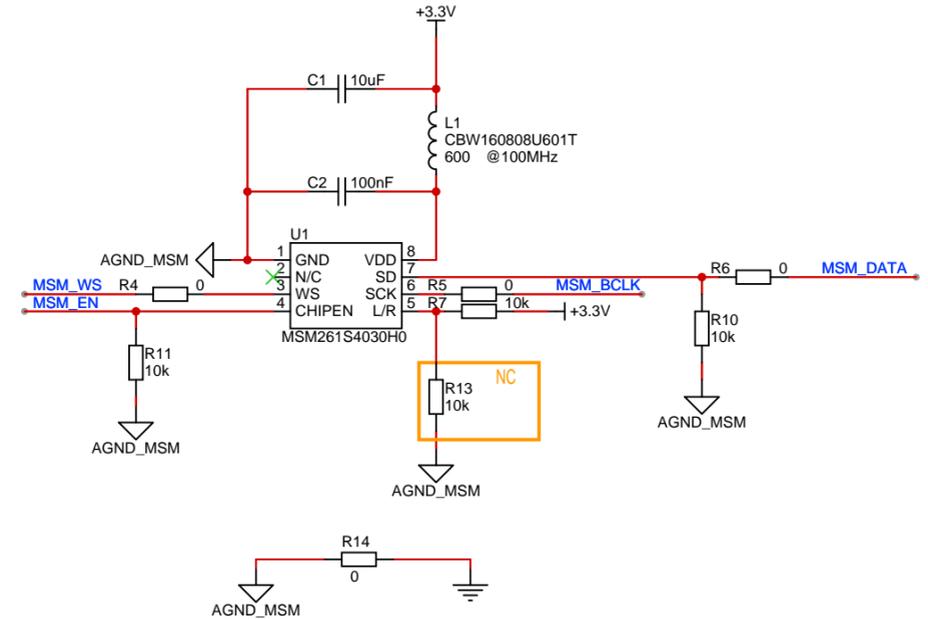
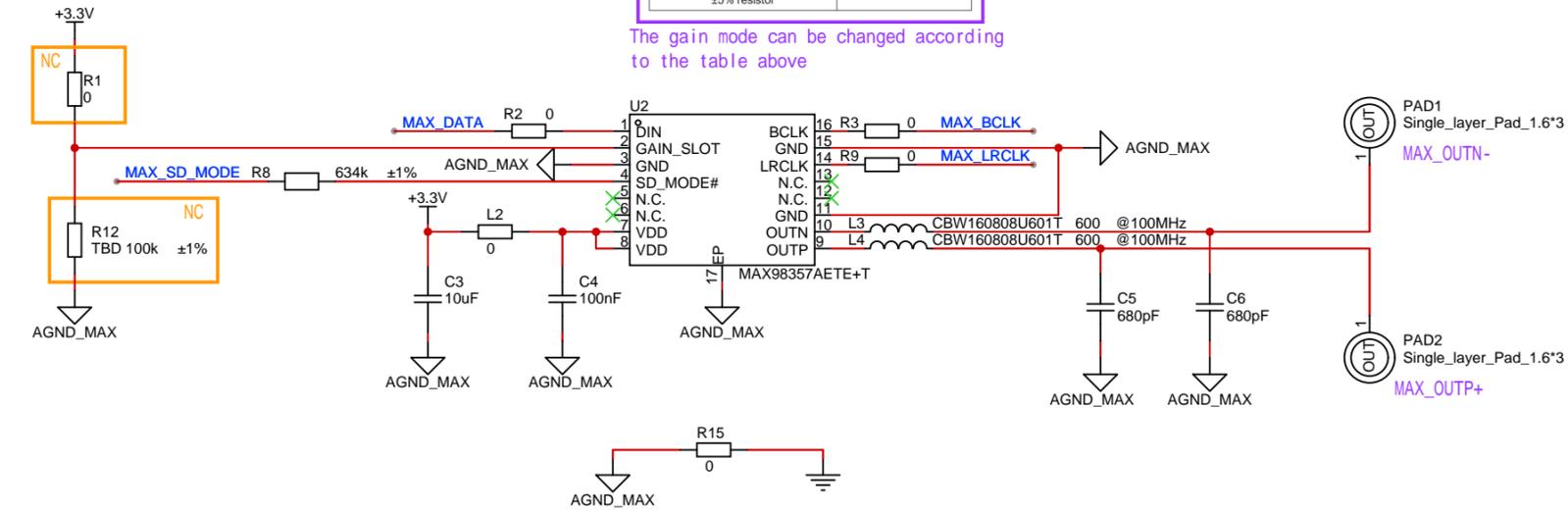
LOGIC VOLTAGE LEVEL (V _{DDIO}) (V)	R _{SMALL} (kΩ)	R _{LARGE} (kΩ)
1.8	69.6	300
3.3	210.2	634

Table 8. Gain Selection

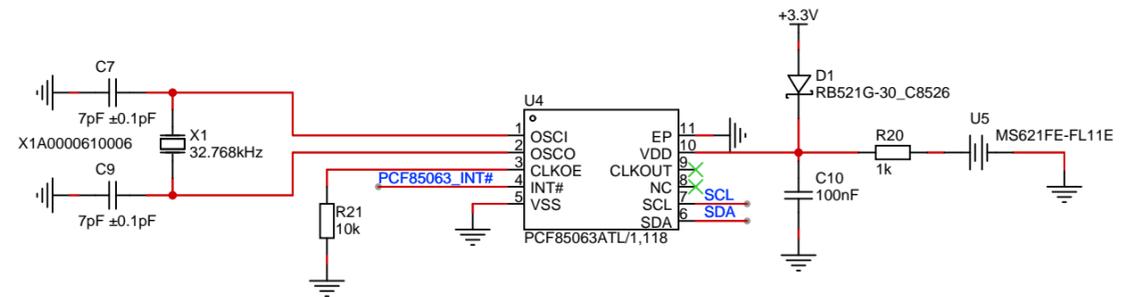
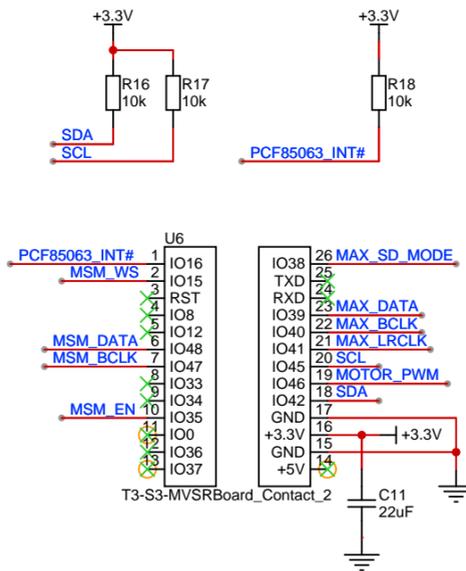
GAIN_SLOT	I ² S/IJ GAIN (dB)
Connect to GND through 100kΩ ±5% resistor	15
Connect to GND	12
Unconnected	9
Connect to V _{DD}	6
Connect to V _{DD} through 100kΩ ±5% resistor	3

Default mode : I2S/LJ GAIN 6dB
 Attention: Over 6dB may cause broken sound

The gain mode can be changed according to the table above



T3-S3



Title: **T3-S3-MVSRBoard** Ver: **V1.0**



Company: **LILYGO**

License: **GPL 3.0**

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